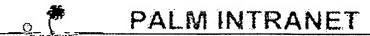
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S3	0	"2002/0163909".pn.	USPAT	OR	OFF	2005/08/05 08:26
S4	1	"0163909"	USPAT	OR	OFF	2005/08/05 08:27
S5	0	Serkinen	USPAT	OR	OFF	2005/08/05 08:28
S6	49	Sarkinen	USPAT	OR	OFF	2005/08/05 08:44
S7	1	"0163909"	USPAT	OR	OFF	2005/08/05 08:45
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S9	1	"6157637".pn.	USPAT	OR	OFF	2005/08/05 10:19
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S11	2503	single adj clock adj cycle\$	USPAT	OR	OFF	2005/08/05 10:49
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S15	0	"20030031178".pn.	USPAT	OR	OFF	2005/08/05 14:26
S16	1	"0031178"	USPAT	OR	OFF	2005/08/05 14:23
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S28	0	"20020163909".pn.	USPAT	OR	ON	2005/08/05 15:24



Day: Sunday Date: 8/7/2005 Time: 10:49:07

## **Inventor Name Search Result**

Your Search was:

Last Name = KIREMIDJIAN
First Name = FREDERICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name		
<u>10163408</u>	Not Issued	030	06/05/2002	BANDWIDTH MANAGEMENT TRAFFIC- SHAPING CELL	KIREMIDJIAN, FREDERICK		
10163407	Not Issued	030	06/05/2002	HETEROGENEOUS NETWORK SWITCH	KIREMIDJIAN, FREDERICK		
10004608	Not Issued	030	11/27/2001	PARALLEL LIMIT CHECKING IN A HIERARCHICAL NETWORK FOR BANDWIDTH MANAGEMENT TRAFFIC- SHAPING CELL	KIREMIDJIAN, FREDERICK		
10004080	Not Issued	041	11/27/2001	BANDWIDTH ALLOCATION CREDIT UPDATING ON A VARIABLE TIME BASIS	KIREMIDJIAN, FREDERICK		
10004079	Not Issued	041	11/27/2001	MULTICAST SERVICE DELIVERY IN A HIERARCHICAL NETWORK	KIREMIDJIAN, FREDERICK		
10004078	Not Issued	041	10/27/2001	VIRTUAL QUEUES IN A SINGLE QUEUE IN THE BANDWIDTH MANAGEMENT TRAFFIC- SHAPING CELL	KIREMIDJIAN, FREDERICK		
09332357	6282169	150	06/11/1999	SERIAL REDUNDANT BYPASS CONTROL MECHANISM FOR MAINTAINING NETWORK BANDWIDTH MANAGEMENT SERVICE	KIREMIDЛAN, FREDERICK		
06745621	4704697	150			KIREMIDJIAN, FREDERICK B.		
06611763	4727475	150	05/18/1984	SELF-CONFIGURING MODULAR COMPUTER SYSTEM WITH AUTOMATIC ADDRESS INIALIZATION	KIREMIDЛAN, FREDERICK		

06311510	Not 161 Issued	APPARATUS AND METHODOLOGY FOR CONFIGURATION OF AN INPUT/OUTPUT DIGITAL CIRCUIT AMONG DIFFERENT INPUT/OUTPUT PROTOCOLS	KIREMIDJIAN, FREDERICK
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Inventor Search Completed: No Records to Display.

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